

Summary

Memory system, and process for controlling a memory component, to achieve different kinds of memory characteristics on one and the same memory component

According to the invention, a memory system, and a process for controlling a memory component, to achieve different kinds of memory characteristics on one and the same memory component, is provided, the process comprising the steps:

- Sending out a signal to select one of several possible modes for the memory component; and
- Operating the memory component in accordance with the specific mode selected by the signal.

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